



IPW

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF:

GROUP: 1775

Motoo ASAI, et al.

SERIAL NO: 10/829,479

ATTENTION:
FILING RECEIPT CORRECTIONS

FILED: April 22, 2004

FOR: MULTILAYER PRINTED WIRING BOARD AND ITS MANUFACTURING
METHOD, AND RESIN COMPOSITION FOR FILLING THROUGH-HOLE

REQUEST FOR CORRECTED OFFICIAL FILING RECEIPT

Office of Initial Patent Examination
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The Patent Office is requested to provide a corrected Official Filing Receipt for the attached. No fees are required. If you have any questions, please do not hesitate to contact us.

Respectfully Submitted,

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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/829,479	04/22/2004	1775	1094	251496US90RE	8	92	14

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CONFIRMATION NO. 5882
UPDATED FILING RECEIPT
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OC000000013577181

Date Mailed: 08/19/2004

Receipt is acknowledged of this reissue Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

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RECEIVED: 8/23/04
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DOCKETING DEPT.

Initials/Date Docketed: dp 8/24/04
Type of Resp(s): _____
Due Date(s): _____

Domestic Priority data as claimed by applicant

This application is a REI of 09/341,689 07/23/1999 PAT 6,376,049
which is a 371 of PCT/JP98/04584 10/12/1998

Foreign Applications

JAPAN 9-280499 10/14/1997
JAPAN 9-340180 12/10/1997
JAPAN 9-340182 12/10/1997
JAPAN 10-067065 03/17/1998

If Required, Foreign Filing License Granted: 06/14/2004

Projected Publication Date: None, application is not eligible for pre-grant publication

Non-Publication Request: No

Early Publication Request: No

Title

Multilayer printed wiring board and its manufacturing method, and resin composition for filling through-hole

Preliminary Class

428

PLEASE NOTE THAT THE DOMESTIC PRIORITY DATA INFORMATION IS
INCORRECT. IT SHOULD READ AS FOLLOWS:

THIS APPLICATION IS A REI OF 09/341,689 07/23/99 PAT 6,376,049
WHICH IS A 371 OF PCT/JP98/04584 10/12/90